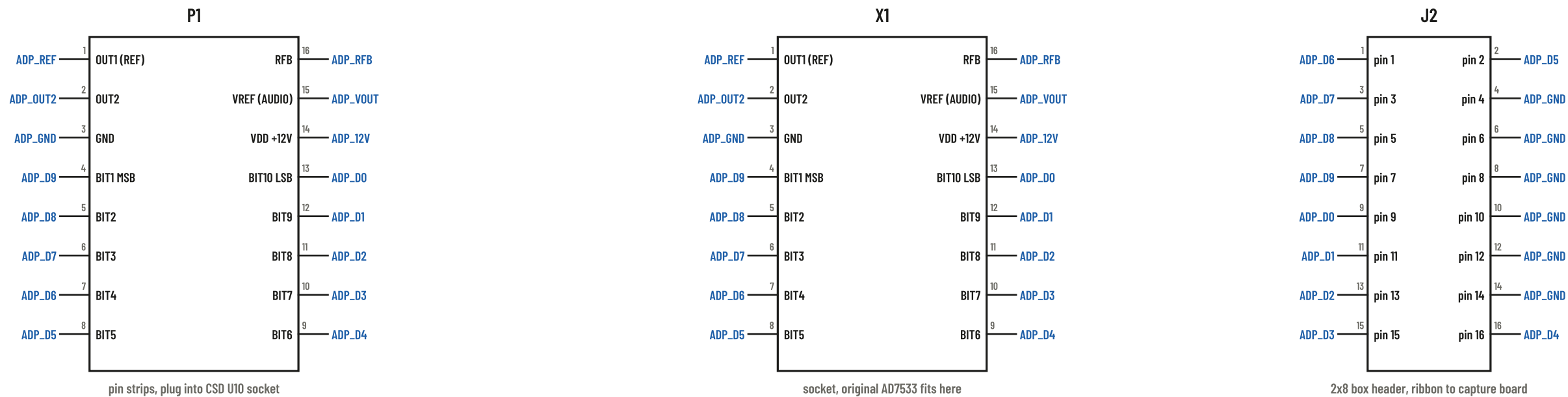


Squeak Tap · Board 1: DAC adapter



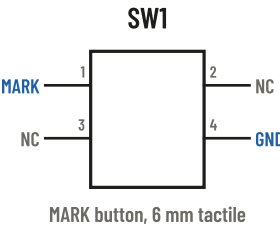
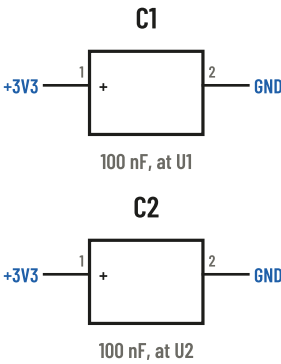
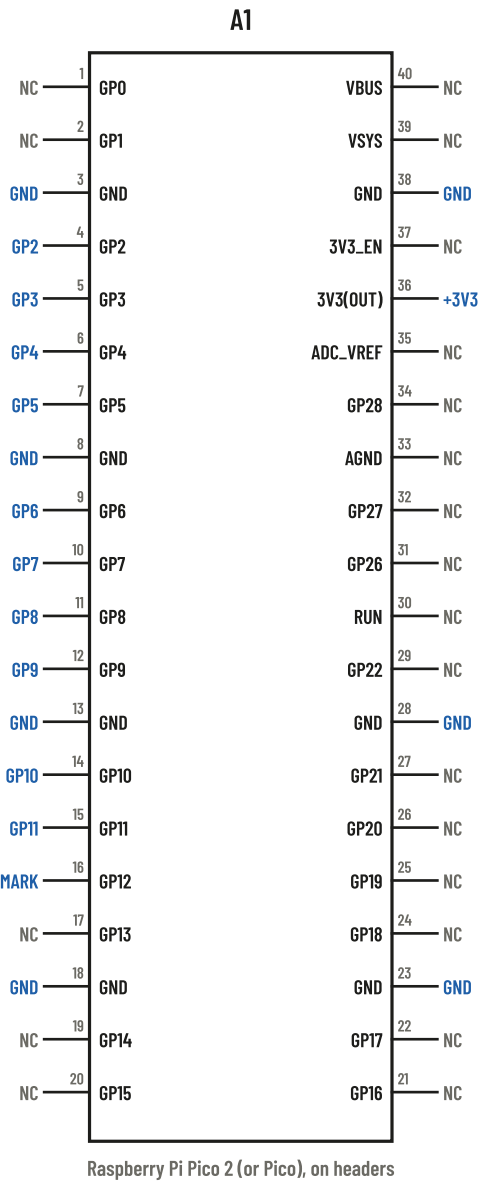
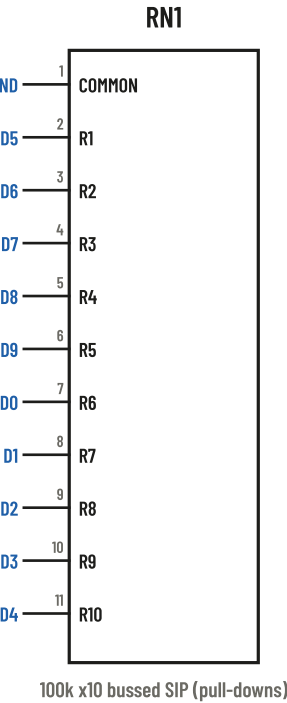
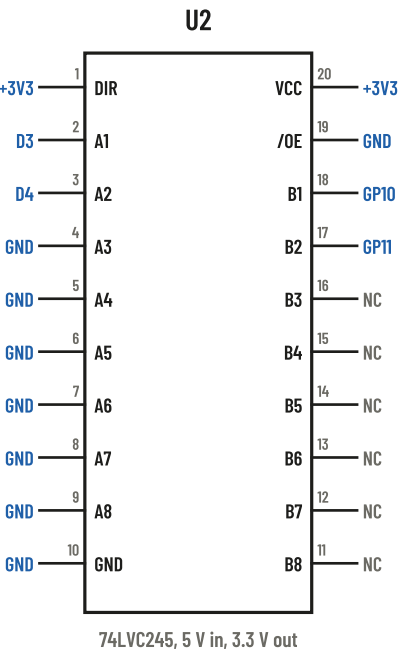
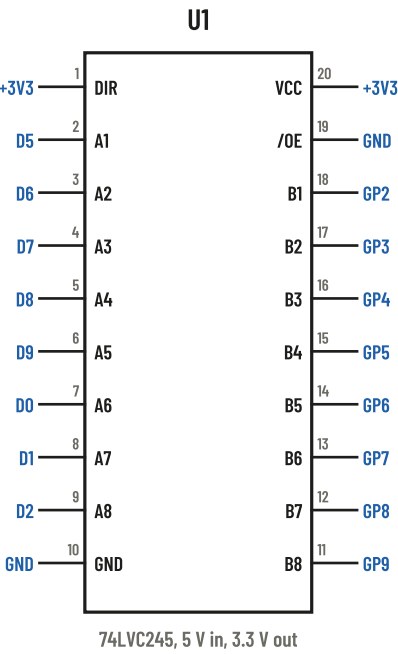
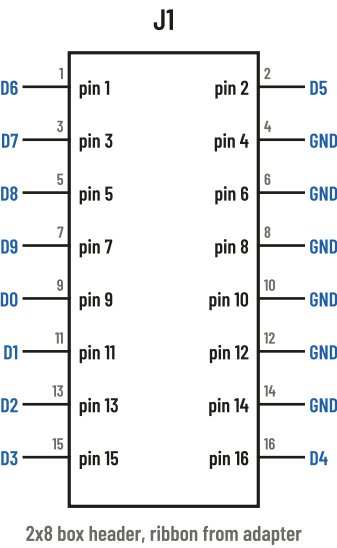
P1 pin n and X1 pin n are joined for all 16 pins: the adapter passes the DAC straight through.

Taps: the 10 data bits (U10 pins 4-13) and ground go to J2. Nothing else is taken from the CSD; no power is used.

CSD U10 wiring (from the A080-91671 schematic): BIT1-BIT8 = PIA U9 PA7-PA0, BIT9 = PB7, BIT10 = PB6.

Midway runs the AD7533 in voltage mode: about 1.7 V from Q2 into OUT1, audio out of VREF (pin 15).

Squeak Tap · Board 2: capture board



Ribbon bit order: U1 A1-A8 = D5-D9, D0-D2; U2 A1-A2 = D3, D4. Firmware rotates GP2-GP11 back to D0-D9.

MARK: SW1 diagonal pads 1 and 4, so the button works whichever way it is fitted. GP12 uses the Pico pull-up.

U2 A3-A8 tied to GND, B3-B8 unused. Both /OE to GND and DIR to 3V3 (A to B).